## Mahatma Education Society's Pillai College of Engineering, New Panvel DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

TIME TABLE INTERNAL ASSESSMENT - II						
B.E. EXTC						
Day	Date	Time	Subject			
Thursday	19 <sup>th</sup> Nov 2020	4pm - 5pm	ME			
Friday	20 <sup>th</sup> Nov 2020	11am - 12pm	MCS			
Friday	20 <sup>th</sup> Nov 2020	4pm - 5pm	OC			
Saturday	21 <sup>st</sup> Nov 2020	11am - 12pm	DLOC : ICE			
Saturday	21 <sup>st</sup> Nov 2020	4pm - 5pm	<u>ILOC :</u> <u>MIS</u> <u>CS</u> <u>OR</u>			

T.E. EXTC				
Day	Date	Time	Subject	
Thursday	19 <sup>th</sup> Nov 2020	4pm - 5pm	MP	
Friday	20 <sup>th</sup> Nov 2020	11am - 12pm	DC	
Friday	20 <sup>th</sup> Nov 2020	4pm - 5pm	EE	
Saturday	21 <sup>st</sup> Nov 2020	11am - 12pm	DTSP	
Saturday	21 <sup>st</sup> Nov 2020	4pm - 5pm	<u>Elective :</u>	
			DCE	

<b>Date</b> 19 <sup>th</sup> Nov 2020	Time	Subject
10 <sup>th</sup> Nov 2020		
19 INOV 2020	4pm - 5pm	AM III
20 <sup>th</sup> Nov 2020	11am - 12pm	EDC
20 <sup>th</sup> Nov 2020	4pm - 5pm	DSD
21 <sup>st</sup> Nov 2020	11am - 12pm	NT
21st Nov 2020	4pm 5pm	EIC
	20 <sup>th</sup> Nov 2020 20 <sup>th</sup> Nov 2020	20 <sup>th</sup> Nov 2020 11am - 12pm   20 <sup>th</sup> Nov 2020 4pm - 5pm   21 <sup>st</sup> Nov 2020 11am - 12pm

**Prof. Florence Simon** 

**Prof. Ameet Mehta** 

Dr. Avinash R. Vaidya

(Test Coordinator)

(Test Coordinator)

(HOD, EXTC)