

Mahatma Education Society's

Pillai College of Engineering, New Panvel

DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING

TIME TABLE (IA-1)

CLASS: T.E. EXTC

SEM- V

Day	Date	Time	Subject
Saturday	28th Aug 2021	10:30am - 11:30am	DCOM
Saturday	28th Aug 2021	1:30pm - 2:30pm	DTSP
Monday	30th Aug 2021	10:30am - 11:30am	DVLSI
Monday	30th Aug 2021	1:30pm - 2:30pm	RSA
Wednesday	1st Sept 2021	1:30pm - 2:30pm	<u>Elective :</u> <u>DCC</u> <u>DSA</u>

Prof. Ameet Mehta

(Test Coordinator)

Prof. Florence Simon

(Test Coordinator)

Dr. Avinash Vaidya

(HOD, EXTC)