Mahatma Education Society's Pillai College of Engineering, New Panvel DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

TIME TABLE (IA-1)

CLASS: S.E. EXTC SEM - IV (A&B)

Day	Date	Time	Subject	Question Paper Marks
Monday	26th Feb. 2024	11.30 AM to 1.00 PM	Engineering Mathematics IV	40
		3.30 PM to 5 PM	Electronic Communication System	40
Tuesday	27th Feb. 2024	11.30 AM to 1.00 PM	Linear Integrated Circuits	40
		3.30 PM to 5 PM	Digital Signal Processing	40
Wednesday	28th Feb. 2024	11.30 AM to 1.00 PM	Microprocessor and Microcontrollers	40
		3.30 PM to 5 PM	Personal Financial Management	20

TIME TABLE (IA-1)

CLASS: T.E. EXTC SEM - VI (A&B)

Day	Date	Time	Subject	Question Paper Marks
Monday	26th Feb. 2024	11.30 AM to 1.00 PM	Wireless and Mobile Communication	40
		3.30 PM to 5 PM	Antenna Theory and Design	40
Tuesday	27th Feb. 2024	11.30 AM to 1.00 PM	Robotics and Automation	40
		3.30 PM to 5 PM	DLOC-Computer Communication & Network	40
			DLOC-Database Management System	40
Wednesday	28th Feb. 2024	1.30 PM to 3.00 PM	ILOC- PD	40
			ILOC-VS	40
			ILOC-EP	40

Prof. Aboli Khedkar
(Test Coordinator)

Prof. Ajit Saraf (Test Coordinator)

Dr. Avinash Vaidya (HOD, EXTC)