

**Mahatma Education Society's
Pillai College of Engineering, New Panvel
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

TIME TABLE (IA-2)

CLASS: S.Y. EXTC SEM - III (A&B)

Day	Date	Time	Subject	Question Paper Marks
Monday	27th Oct. 2025	09.30 AM to 11.00 AM	Engineering Mathematics III	40
		1.30 PM to 2.30 PM	Personal Finance Management	20
Tuesday	28th Oct. 2025	09.30 AM to 11.00 AM	Network Theory	40
		1.30 PM to 3.00 PM	Digital System Design	40
Wednesday	29th Oct. 2025	09.30 AM to 11.00 AM	Electronics Devices	40
		1.30 PM to 3.00 PM	Signals and Systems	40

TIME TABLE (IA-1)

CLASS: T.Y. EXTC SEM - V (A&B)

Day	Date	Time	Subject	Question Paper Marks
Monday	27th Oct. 2025	09.30 AM to 11.00 AM	Digital Communication	40
		1.30 PM to 3.00 PM	DLOC II: Computer Communication & Network	40
Tuesday	28th Oct. 2025	09.30 AM to 11.00 AM	DLOC I: Basics of VLSI Design	40
		1.30 PM to 3.00 PM	ILOC: Quantum Computing & Technologies	40
			ILOC: Cooking and Nutrition	40
Wednesday	29th Oct. 2025	09.30 AM to 11.00 AM	Digital Time Signal Processing	40

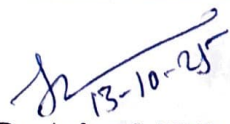
TIME TABLE (IA-1)

CLASS: BTech EXTC SEM - VII (A&B)

Day	Date	Time	Subject	Question Paper Marks
Monday	27th Oct. 2025	09.30 AM to 11.00 AM	Microwave & RF Design	40
Tuesday	28th Oct. 2025	09.30 AM to 11.00 AM	DLOC: Cloud Computing	40
Wednesday	29th Oct. 2025	09.30 AM to 11.00 AM	IoT driven App Development	40
		3.30 PM to 5.00 PM	ILOC: Business Analytics	40
			ILOC: Journalism, Media and Communication studies	40
Thursday	30th Oct. 2025	09.30 AM to 1.00 PM	DLOC: Data Science	40


Prof. Aboli Khedkar
(Test Coordinator)


Dr. Karpagavalli S.
(Test Coordinator)


Dr. Avinash Vaidya
(HOD, EXTC)