

**Mahatma Education Society's  
Pillai College of Engineering, New Panvel  
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

**TIME TABLE (IA-2)**

**CLASS: S.Y. EXTC SEM - IV (A&B)**

Day	Date	Time	Subject	Question Paper Marks
Friday	10th Apr. 2026	09.30 AM to 11.00 AM	Engineering Mathematics IV	40
		1.30 PM to 3.00 PM	Electronic Communication Systems	40
Saturday	11th Apr. 2026	09.30 AM to 11.00 AM	Digital Signal Processing	40
		1.30 PM to 3.00 PM	Linear Integrated Circuits	40
Monday	13th Apr. 2026	09.30 AM to 11.00 AM	Microprocessor and Microcontroller	40

**TIME TABLE (IA-2)**

**CLASS: T.Y. EXTC SEM - VI (A&B)**

Day	Date	Time	Subject	Question Paper Marks
Friday	10th Apr. 2026	09.30 AM to 11.00 AM	Electromagnetic Wave & Radiating Systems	40
		1.30 PM to 2.30 PM	Data Structures	20
Saturday	11th Apr. 2026	09.30 AM to 11.00 AM	DLOC-I: Robotics and Automation	40
			DLOC-I: Integrated Circuit Technology	40
		1.30 PM to 3.00 PM	DLOC-II: Advanced Networking Technologies	40
Monday	13th Apr. 2026	09.30 AM to 11.00 AM	Embedded Systems	40
		1.30 PM to 3.00 PM	ILOC: QCQT-2	40

*Aboli Khedkar*  
4/4/26  
**Prof. Aboli Khedkar**  
(Test Coordinator)

*S. Karpavalli*  
**Dr. Karpavalli S.**  
(Test Coordinator)

*Avinash Vaidya*  
4-4-26  
**Dr. Avinash Vaidya**  
(HOD, EXTC)