

PROGRAMME - SECOND YEAR (COMPUTER ENGINEERING)
END SEMESTER EXAMINATION TIME TABLE (NOVEMBER 2026)
SEMESTER-III(ATKT)

Academic Year		2022-2023		2023-2024		2024-2025		2025-2026	
Days and Dates	Time	Subject Code	Subject	Subject Code	Subject	Subject Code	Subject	Subject Code	Subject
Monday, 16 November, 2026	11.00 AM TO 01.00 PM	CE 201	Engineering Mathematics III	CE 201	Engineering Mathematics III	CE 201	Engineering Mathematics III	MATH201C	Engineering Mathematics III
Wednesday, 18 November, 2026	11.00 AM TO 01.00 PM	CE 202	Data Structures	CE 202	Data Structures	CE 202	Data Structures	CE 201	Data Structure
Friday, 20 November, 2026	11.00 AM TO 01.00 PM	CE 205	Paradigms and Computer Programming Fundamentals	CE 205	Database Management Systems	CE 203	Database Management Systems	CE 202	Database Management Systems
Monday, 23 November, 2026	11.00 AM TO 01.00 PM	CE 204	Digital Logic and Computer Organization and Architecture	CE 204	Digital Logic and Computer Architecture	CE 204	Digital Logic and Computer Architecture	CE 203	Digital Electronics
Wednesday, 25 November, 2026	11.00 AM TO 01.00 PM	CE 203	Computer Graphics Virtual Reality	CE 203	Computer Graphics Virtual Reality			CE204	Computer Organization and Architecture

PROGRAMME - SECOND YEAR (COMPUTER ENGINEERING)

INTERNAL ASSESSEMENT EXAMINATION TIME TABLE (NOVEMBER 2026)

SEMESTER-III(ATKT)

Academic Year		2022-2023		2023-2024		2024-2025		2025-2026	
Days and Dates	Time	Subject Code	Subject	Subject Code	Subject	Subject Code	Subject	Subject Code	Subject
Monday, 16 November, 2026	03.00 PM TO 04.30 PM	CE 201	Engineering Mathematics III	CE 201	Engineering Mathematics III	CE 201	Engineering Mathematics III	MATH201C	Engineering Mathematics III
Wednesday, 18 November, 2026	03.00 PM TO 04.30 PM	CE 202	Data Structures	CE 202	Data Structures	CE 202	Data Structures	CE 201	Data Structure
Friday, 20 November, 2026	03.00 PM TO 04.30 PM	CE 205	Paradigms and Computer Programming Fundamentals	CE 205	Database Management Systems	CE 203	Database Management Systems	CE 202	Database Management Systems
Monday, 23 November, 2026	03.00 PM TO 04.30 PM	CE 204	Digital Logic and Computer Organization and Architecture	CE 204	Digital Logic and Computer Architecture	CE 204	Digital Logic and Computer Architecture	CE 203	Digital Electronics
Wednesday, 25 November, 2026	03.00 PM TO 04.30 PM	CE 203	Computer Graphics Virtual Reality	CE 203	Computer Graphics Virtual Reality			CE204	Computer Organization and Architecture

Important Note: • Change if any, in the time table shall be communicated on the college web site.

New Panvel
11th SEPTEMBER, 2026.




PRINCIPAL
MAHATMA EDUCATION SOCIETY'S
PILLAI COLLEGE OF ENGINEERING (AUTONOMOUS)
Dr. K.M. Vasudevan Pillai Campus, Sector-16
New Panvel-410206, Navi Mumbai, Maharashtra, INDIA